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Docket No.: SC13161TP
Applicant: Leo Mathew et al.
Serial No.: 10/705,504
Art Unit: 2818
Filed: November 10, 2003
Class: 257-270000

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	X	1 page Facsimile Cover Sheet
2.	X	1 page PTOL-65B Issue Fee Transmittal (in duplicate)
3.	X	1 page Issue of Patent to Assignee Pursuant to 37 CFR 3.81
4.	X	2 page Comments on Statement of Reasons for Allowance

Paid by Deposit Account: 503079, Freescale Semiconductor, Inc. \$1630

PA

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DOCKET NO. SC13161TP

UNITED STATES PATENT AND TRADEMARK OFFICE
 APPLICANT Leo Mathew et al. GROUP ART UNIT: 2818
 APPLN. NO.: 10/705,504 EXAMINER: Tu-Tu V Ho
 FILED: November 10, 2003 CONFIRMATION No.: 4770
 TITLE: INTEGRATED CIRCUIT HAVING MULTIPLE MEMORY
 TYPES AND METHOD OF FORMATION

Certificate of Transmission under 37 C.F.R. 1.8

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

This paper is filed contemporaneous with the filing of the issue fee for this application. Entrance of the comments provided below regarding the statement of reasons for allowance provided by the Examiner is respectfully requested.

FEES

Applicants authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds.



DOCKET NO. SC13161TP

REMARKS

In a communication mailed September 7, 2004, claims 1-20 were allowed. The Examiner provided reasons for allowance of the independent claims 1, 7 and 15. The reasons provided were comprehensive and general by listing various claim recitals from the independent claims. Therefore, it is clear from the record that the Examiner's quotation of specific portions of the allowed claims was illustrative and the basis for novelty was not limited to a specific recital.

Respectfully submitted,

SEND CORRESPONDENCE TO:

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